

CM150DY-12NF



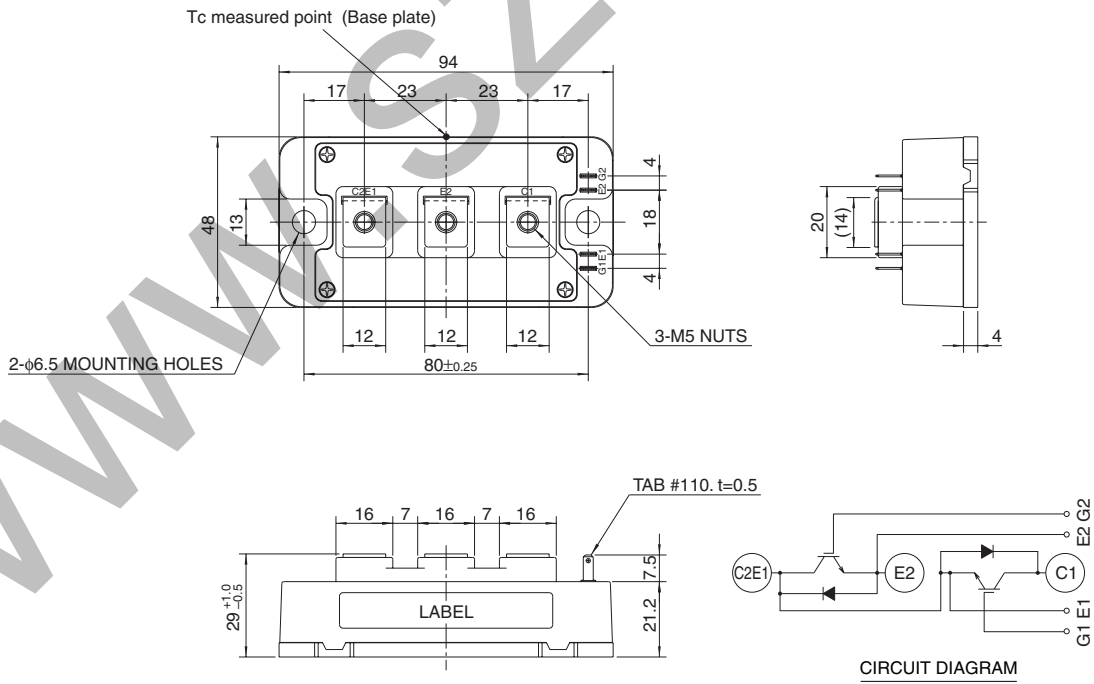
- IC 150A
- VCES 600V
- Insulated Type
- 2-elements in a pack

APPLICATION

General purpose inverters & Servo controls, etc

OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



MAXIMUM RATINGS (T_j = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V _{CES}	Collector-emitter voltage	G-E Short	600	V
V _{GES}	Gate-emitter voltage	C-E Short	±20	V
I _C	Collector current	DC, T _c ' = 97°C ³	150	A
I _{CM}		Pulse (Note 2)	300	A
I _E (Note 1)	Emitter current		150	A
I _{EM} (Note 1)		Pulse (Note 2)	300	A
P _C (Note 3)	Maximum collector dissipation	T _c = 25°C	590	W
T _j	Junction temperature		-40 ~ +150	°C
T _{stg}	Storage temperature		-40 ~ +125	°C
V _{iso}	Isolation voltage	Terminals to base plate, f = 60Hz, AC 1 minute	2500	V _{rms}
—	Torque strength	Main terminals M5 screw	2.5 ~ 3.5	N • m
—		Mounting M6 screw	3.5 ~ 4.5	N • m
—	Weight	Typical value	310	g

ELECTRICAL CHARACTERISTICS (T_j = 25°C, unless otherwise specified)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
I _{CES}	Collector cutoff current	V _{CE} = V _{CES} , V _{GE} = 0V	—	—	1	mA
V _{GE(th)}	Gate-emitter threshold voltage	I _C = 15mA, V _{CE} = 10V	5	6	7.5	V
I _{GES}	Gate leakage current	±V _{GE} = V _{GES} , V _{CE} = 0V	—	—	0.5	μA
V _{CE(sat)}	Collector-emitter saturation voltage	I _C = 150A, V _{GE} = 15V				
		T _j = 25°C	—	1.7	2.2	V
		T _j = 125°C	—	1.7	—	V
C _{ies}	Input capacitance	V _{CE} = 10V	—	—	23	nF
C _{oes}	Output capacitance	V _{GE} = 0V	—	—	2.8	nF
C _{res}	Reverse transfer capacitance		—	—	0.9	nF
Q _G	Total gate charge	V _{CC} = 300V, I _C = 150A, V _{GE} = 15V	—	600	—	nC
t _{d(on)}	Turn-on delay time		—	—	120	ns
t _r	Turn-on rise time	V _{CC} = 300V, I _C = 150A	—	—	100	ns
t _{d(off)}	Turn-off delay time	V _{GE} = ±15V	—	—	300	ns
t _f	Turn-off fall time	R _G = 4.2Ω, Inductive load	—	—	300	ns
t _{rr} (Note 1)	Reverse recovery time	I _E = 150A	—	—	150	ns
Q _{rr} (Note 1)	Reverse recovery charge		—	2.5	—	μC
V _{EC} (Note 1)	Emitter-collector voltage	I _E = 150A, V _{GE} = 0V	—	—	2.6	V
R _{th(j-c)Q}	Thermal resistance ^{*1}	IGBT part (1/2 module)	—	—	0.21	K/W
R _{th(j-c)R}		FWDi part (1/2 module)	—	—	0.47	K/W
R _{th(c-f)}	Contact thermal resistance	Case to heat sink, Thermal compound Applied ^{*2} (1/2 module)	—	0.07	—	K/W
R _{th(j-c')Q}	Thermal resistance	Case temperature measured point is just under the chips	—	—	0.16 ^{*3}	K/W
R _G	External gate resistance		4.2	—	42	Ω

*1 : Case temperature (T_c) measured point is shown in page OUTLINE DRAWING.

*2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].

*3 : Case temperature (T_c) measured point is just under the chips.

If you use this value, R_{th(f-a)} should be measured just under the chips.

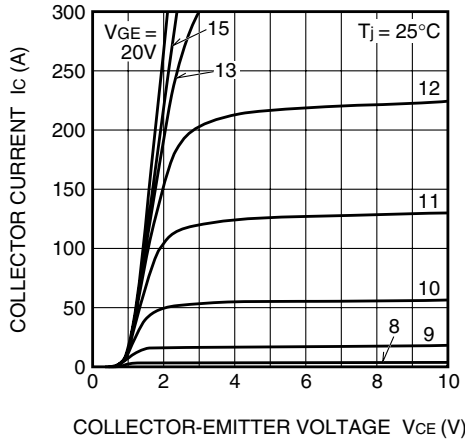
Note 1. I_E, V_{EC}, t_{rr} & Q_{rr} represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).

2. Pulse width and repetition rate should be such that the device junction temperature (T_j) does not exceed T_{jmax} rating.

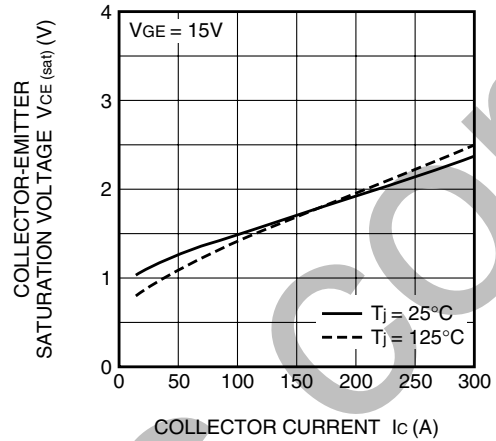
3. Junction temperature (T_j) should not increase beyond 150°C.

PERFORMANCE CURVES

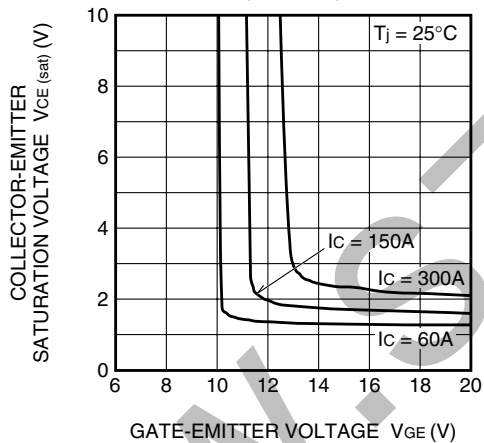
OUTPUT CHARACTERISTICS (TYPICAL)



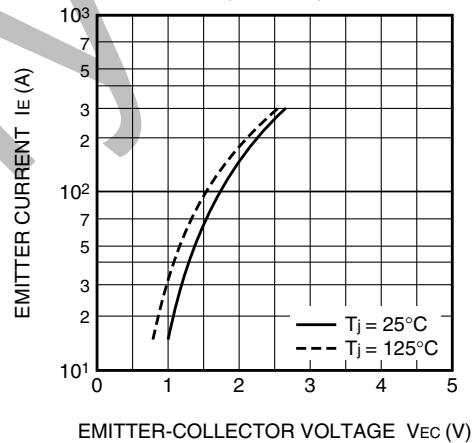
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



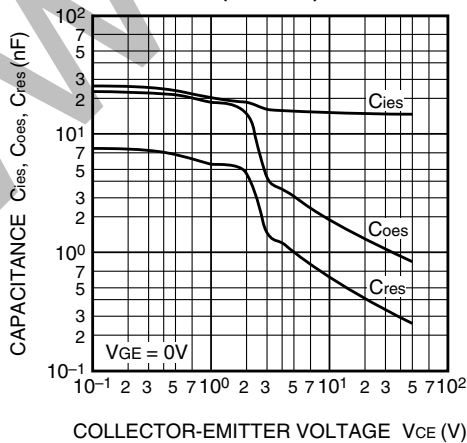
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



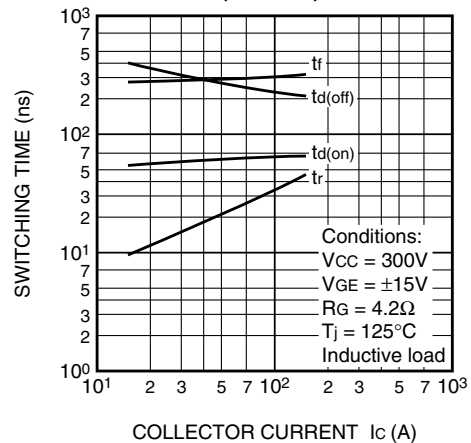
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



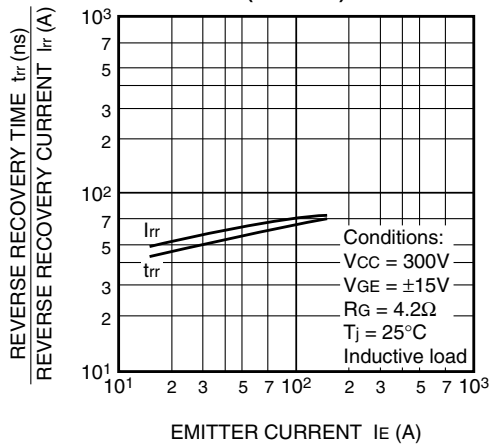
CAPACITANCE-VCE CHARACTERISTICS (TYPICAL)



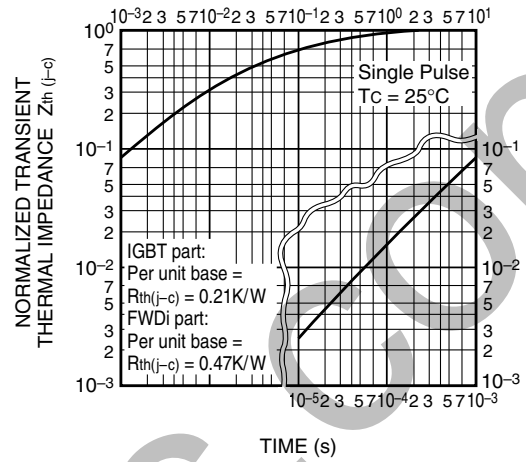
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



REVERSE RECOVERY CHARACTERISTICS OF FREE-WHEEL DIODE (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (IGBT part & FWDi part)



GATE CHARGE CHARACTERISTICS (TYPICAL)

